

Prior Art  
Fig. 1

200

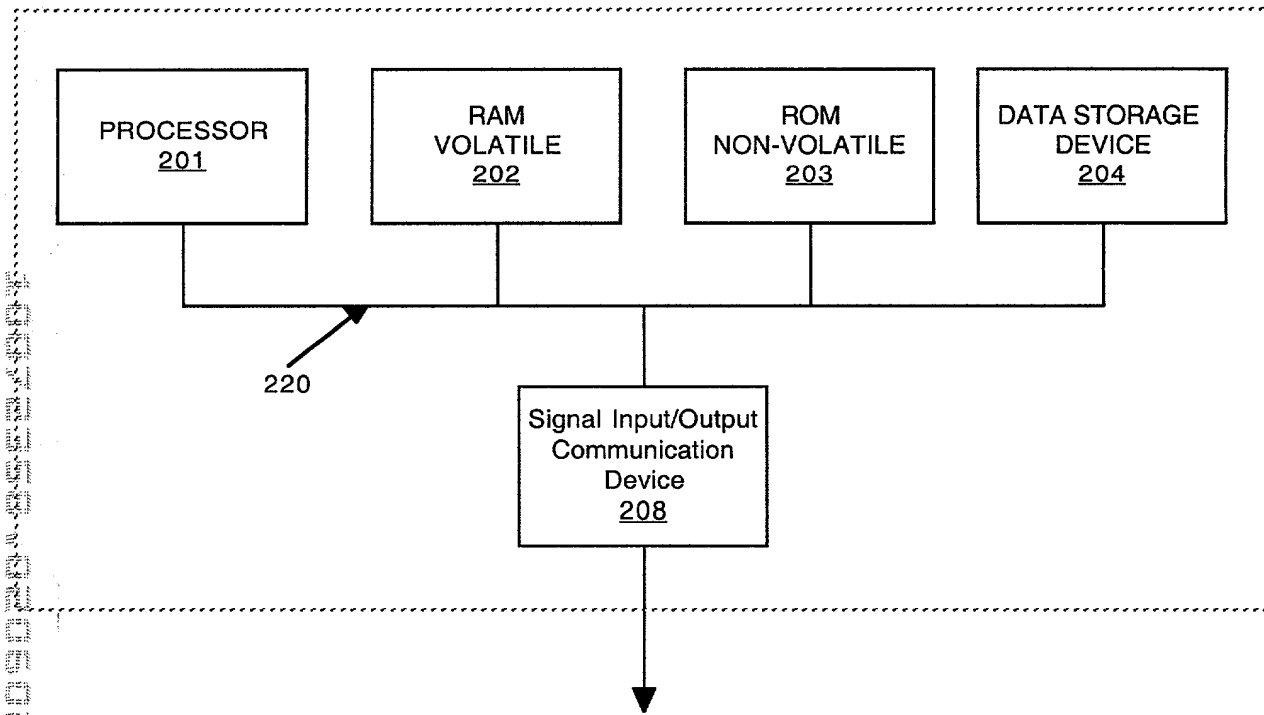
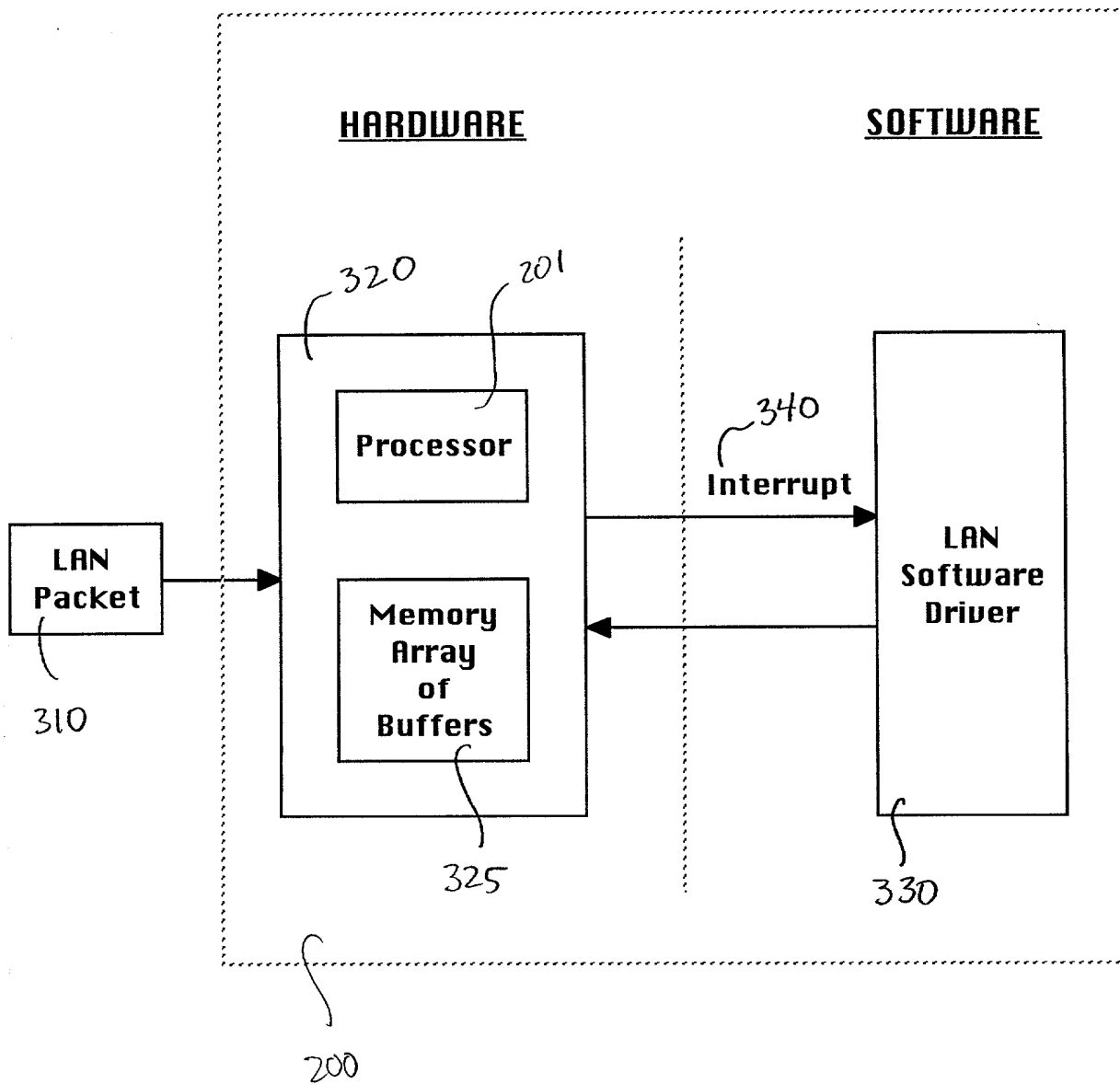


Figure 2

FIG. 3 is a block diagram of a LAN system 200. The LAN system 200 includes a LAN packet 310, a processor 320, a memory array of buffers 325, and a LAN software driver 330. The processor 320 and memory array of buffers 325 are part of the hardware 201, while the LAN software driver 330 is part of the software 202. The LAN packet 310 is received by the processor 320, which then stores it in the memory array of buffers 325. The LAN software driver 330 is notified of the arrival of the packet via an interrupt 340 and retrieves the packet from the memory array of buffers 325.



**Fig. 3**

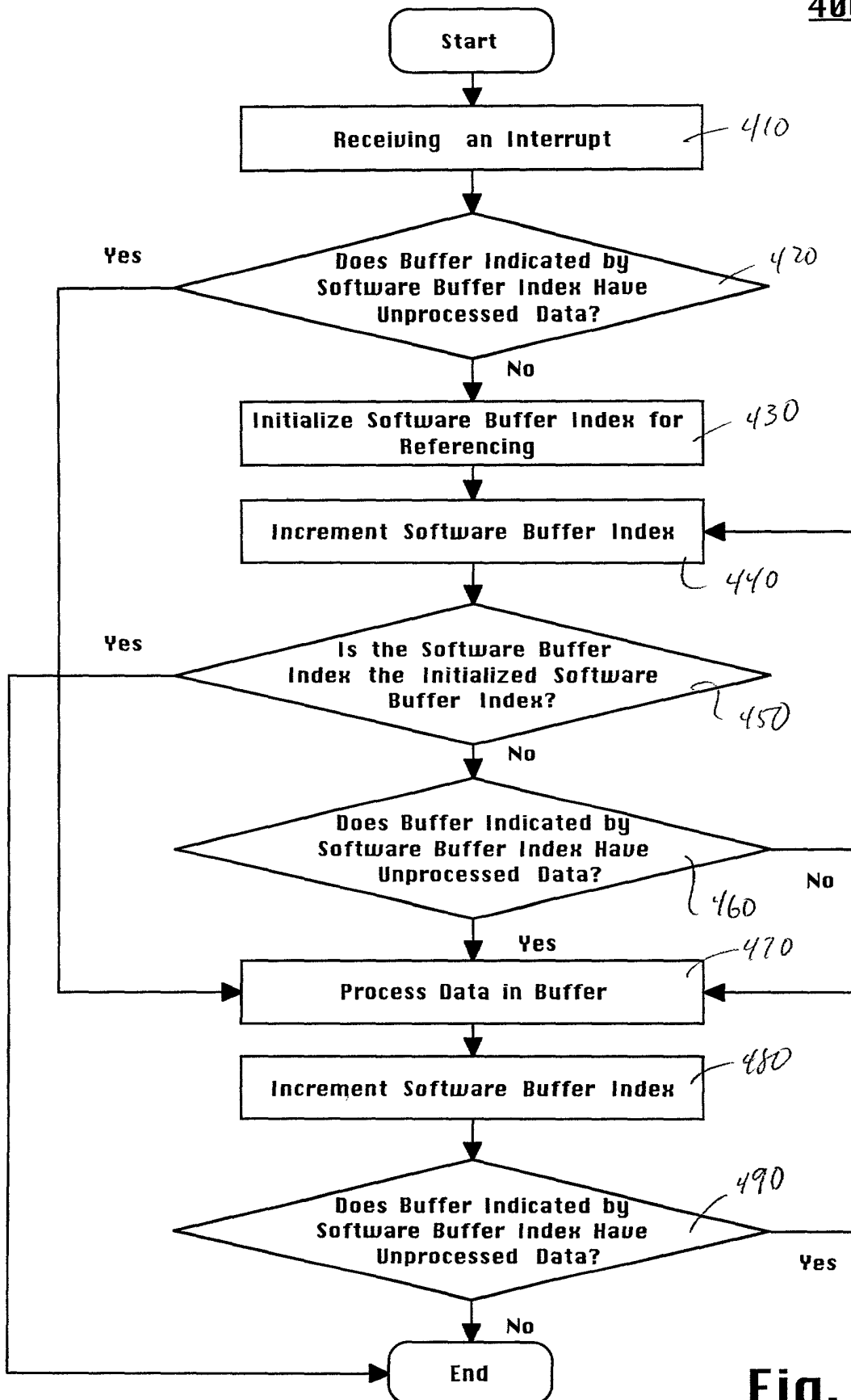


Fig. 4

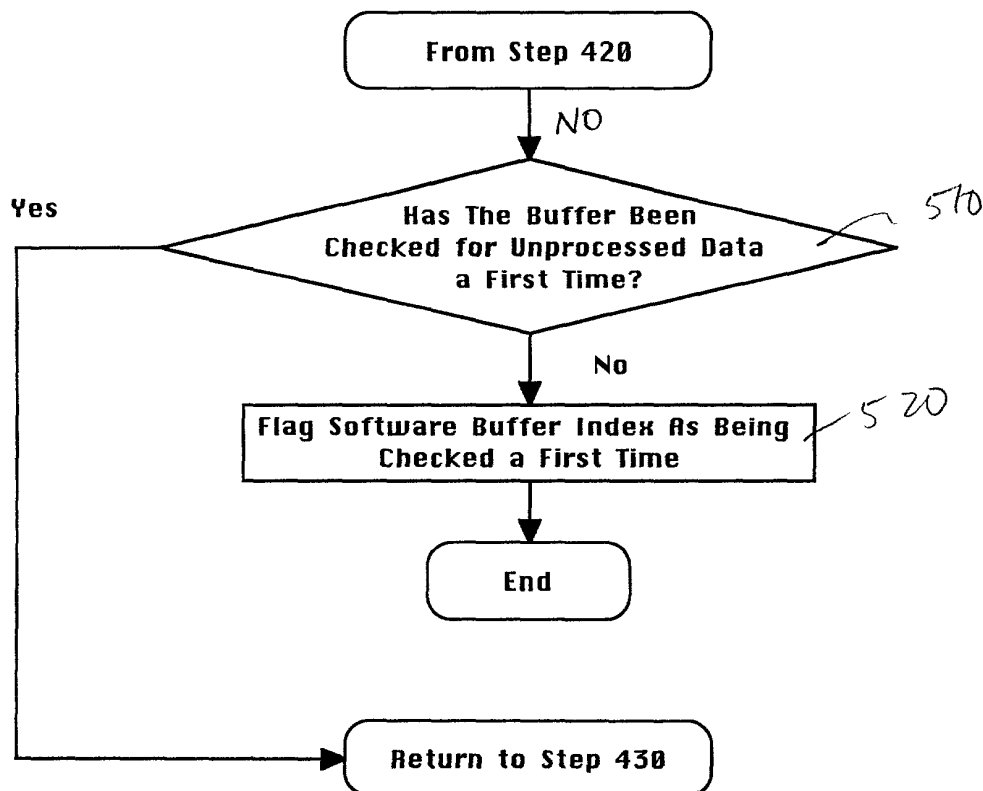


Fig. 5

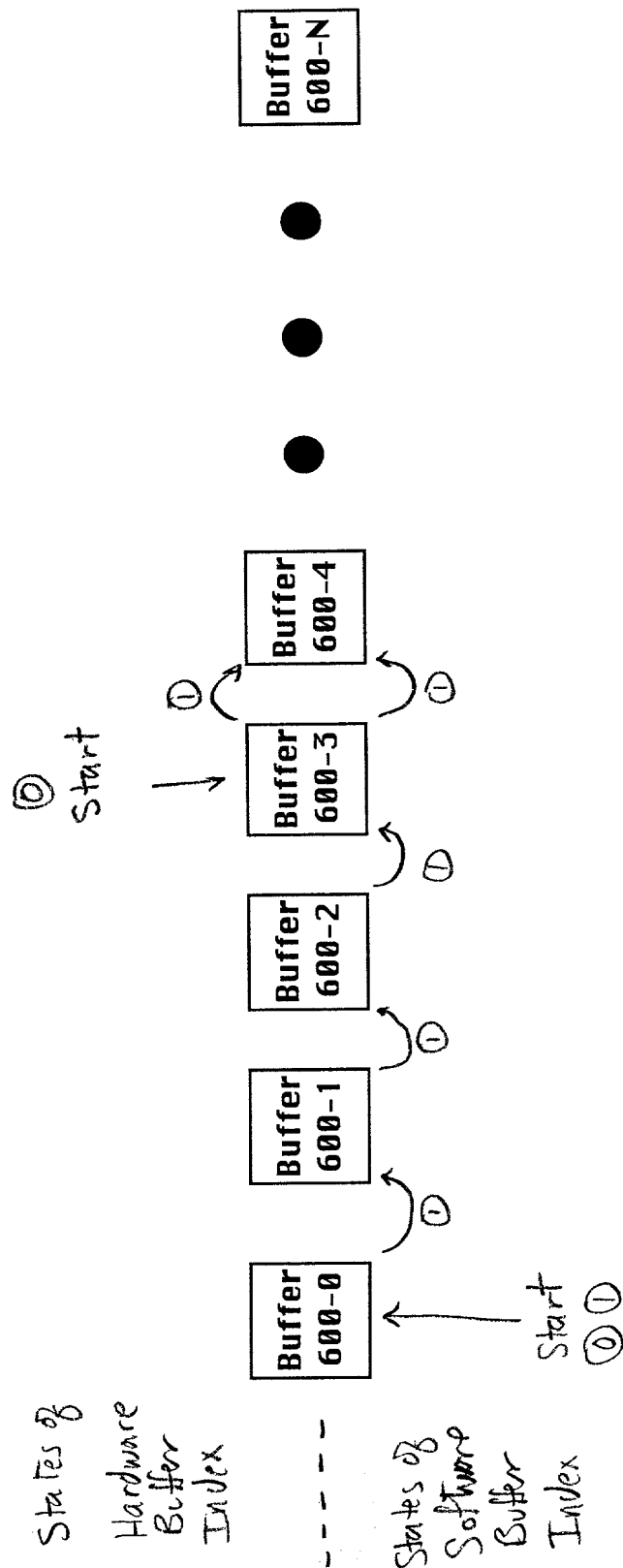


Fig. 6